**DESIGN AND VERIFICATION OF ADVANCED MICROCONTROLLER BUS ARCHITECTURE-ADVANCED PERIPHERAL BUS (AMBA-APB) PROTOCOL**

**PROJECT REPORT**

SUBMITTED IN PARTIAL FULFILLMENT OF THE EQUIREMENTS FOR THE AWARD OF THE DEGREE OF

**BACHELOR OF ENGINEERING**

**(ELECTRONICS AND COMMUNICATION)**

In

THE FACULTY OF ENGINEERING TECHNOLOGY

Submitted by

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***BONAFIDE CERTIFICATE***

*Certified that this thesis is the bonafide work of* **T. ARUNKUMAR, S. EASWARAN, S. PRASANTH** *who has carried out the project under my supervision. Certified further that to the best of my knowledge the work reported here in does not form part of other thesis or dissertation on the basis of which a degree or award was conferred on earlier occasion on this or any other candidate.*

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**ACKNOWLEDGMENTS**

We express our sincerethanks to **Dr. P. SHANMUGA RAJA,** Professor, Department of Electronics and Communication Engineering for her valuable guidance, useful suggestions and encouragement in the conception and completion of this thesis work. Without her expert guidance this project would not have seen the light of the day. We wish to **Dr. R. JOSEPH DANIEL,** Professor & Head, Department of Electronics and Communication Engineering, for valuable suggestions, kind support and constant encouragement. We also wish to express our thanks to our teaching and non – teaching staff of our department for their help and support. Deep from our heart, our thanks go to our family and our friends for their timely help on several occasions to complete the thesis work. I would like thanks my project co-ordinator **Dr. P. SHANMUGA RAJA** Professor, Department of Electronics and Communication Engineering, who permitted us to work on this topic.

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